

FIG.1

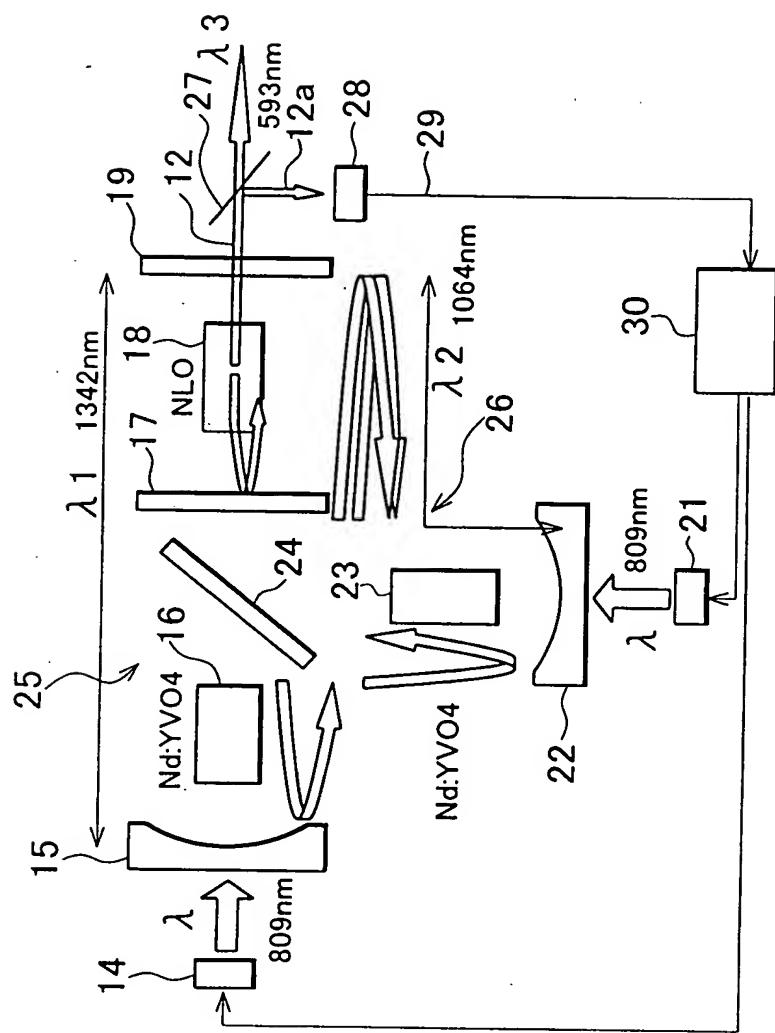


FIG.2A

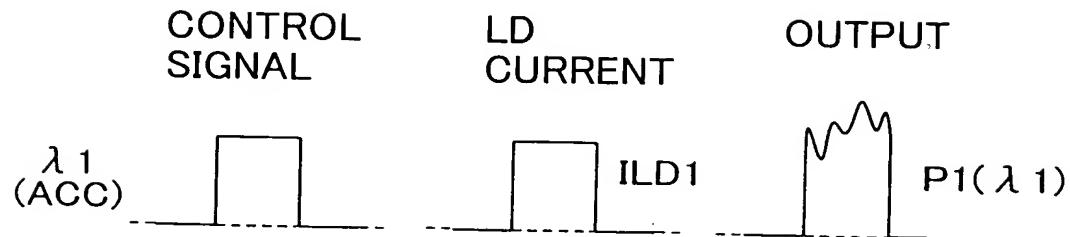


FIG.2B

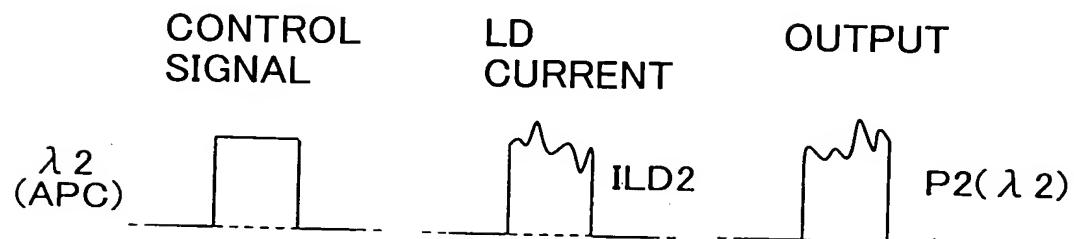


FIG.2C



FIG.3A

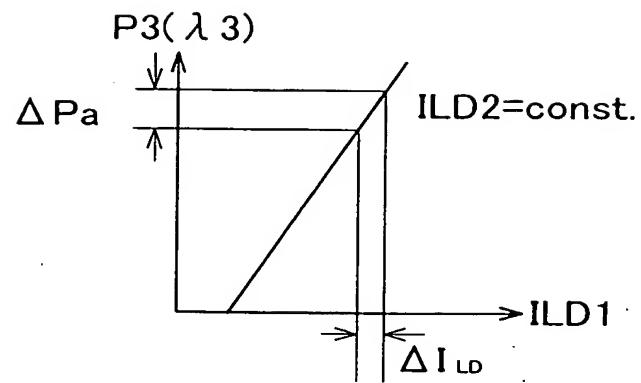


FIG.3B

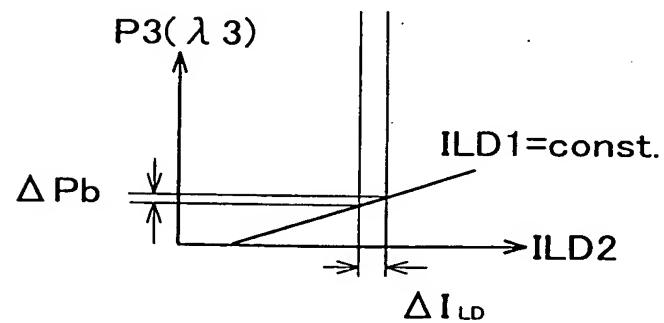


FIG.4

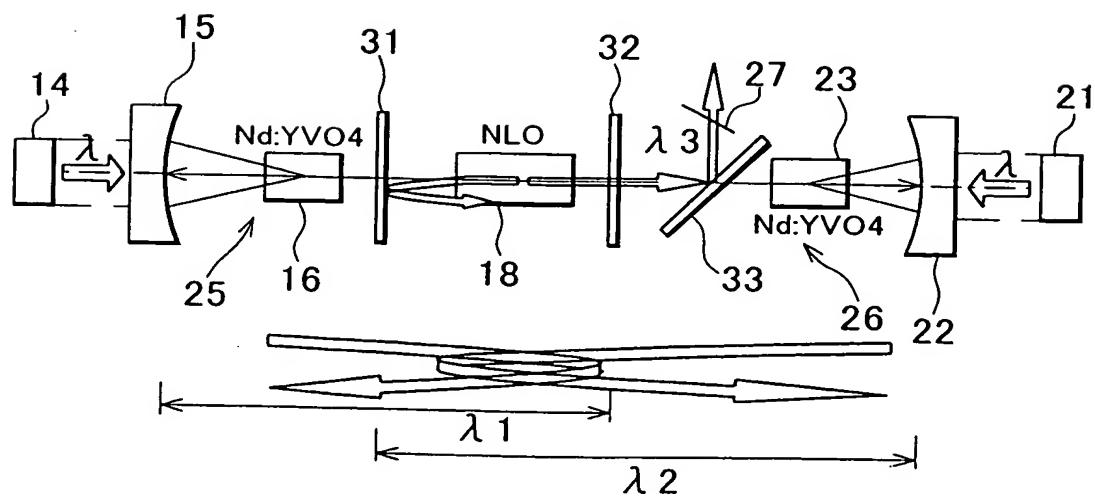


FIG.5 (PRIOR ART)

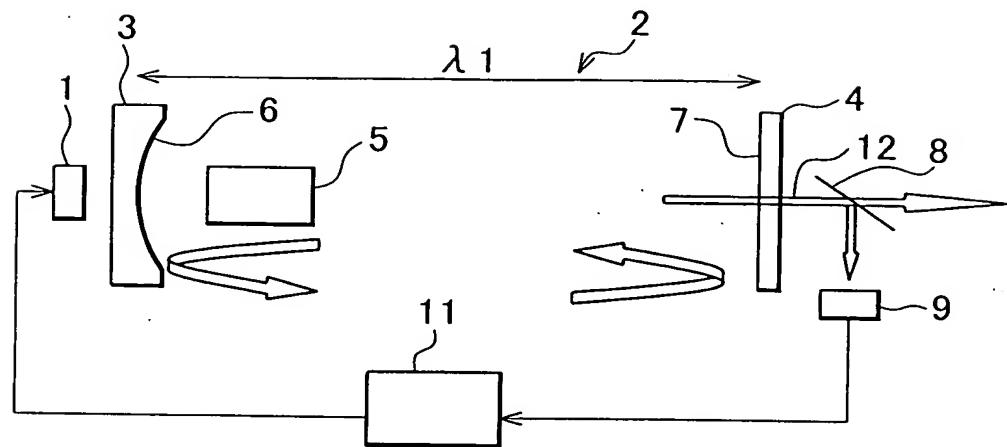


FIG.6A
(PRIOR ART) CONTROL SIGNAL



FIG.6B
(PRIOR ART) LD CURRENT



FIG.6C
(PRIOR ART) OUTPUT

